

FIG.1

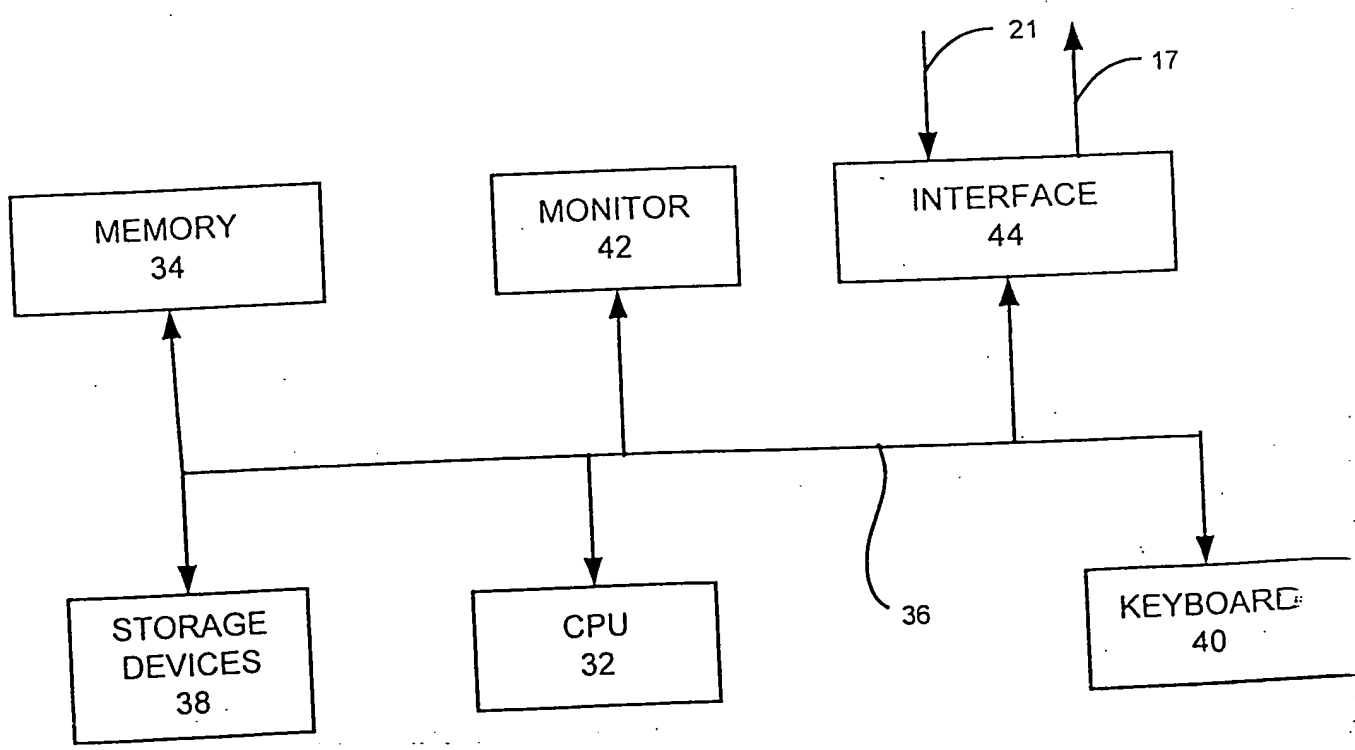


FIG.2

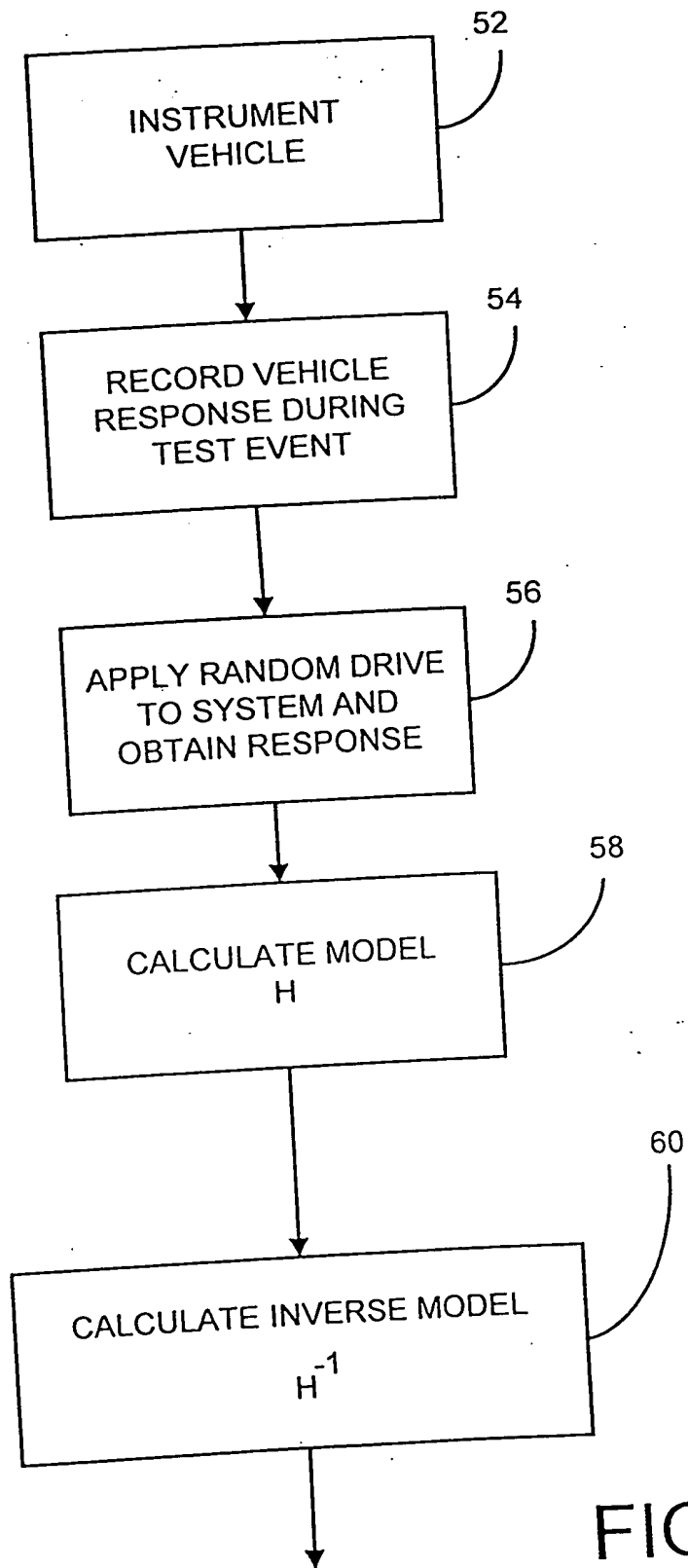


FIG. 3A

FIG. 3B  
OR  
FIG. 3C

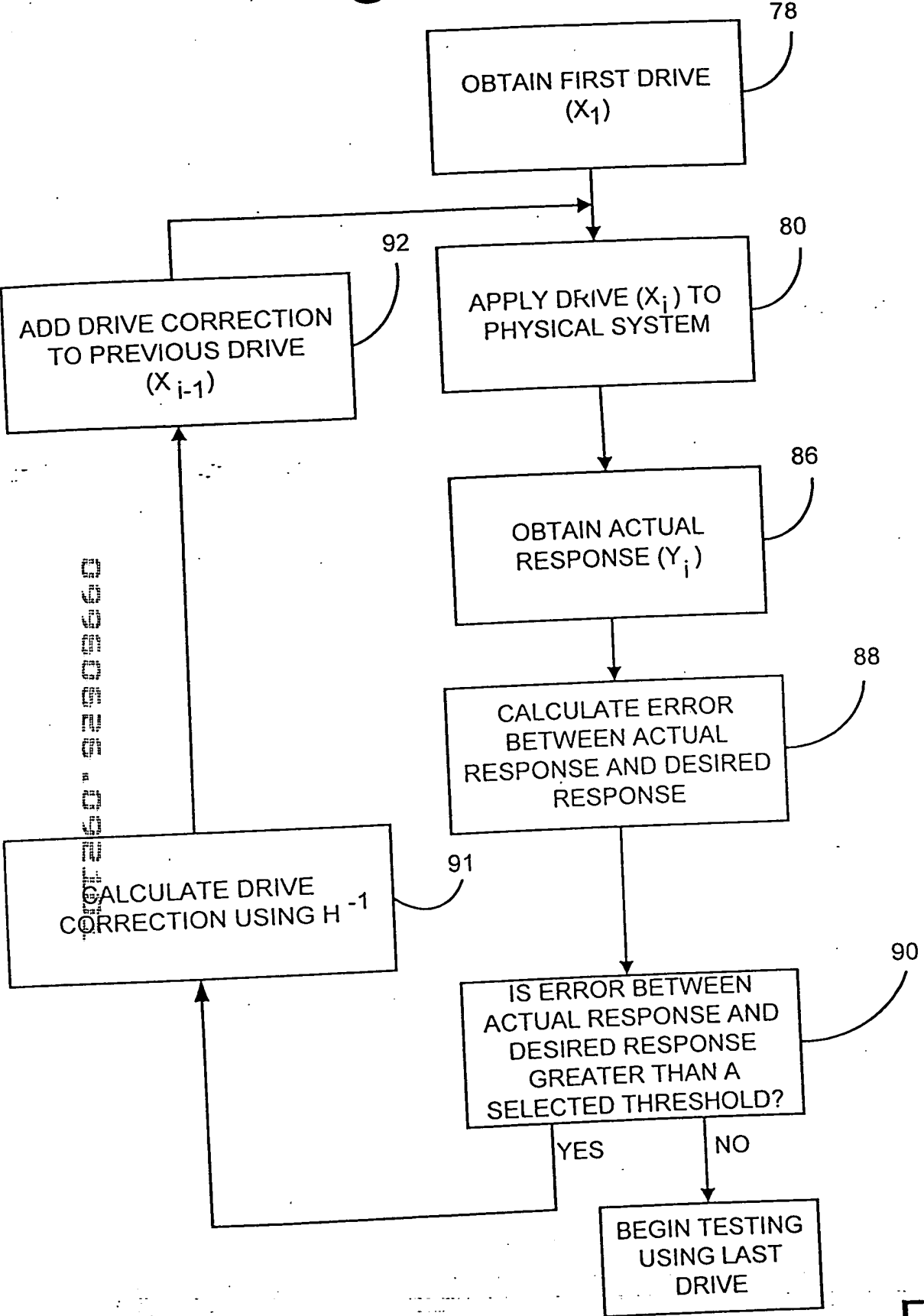


FIG. 3B

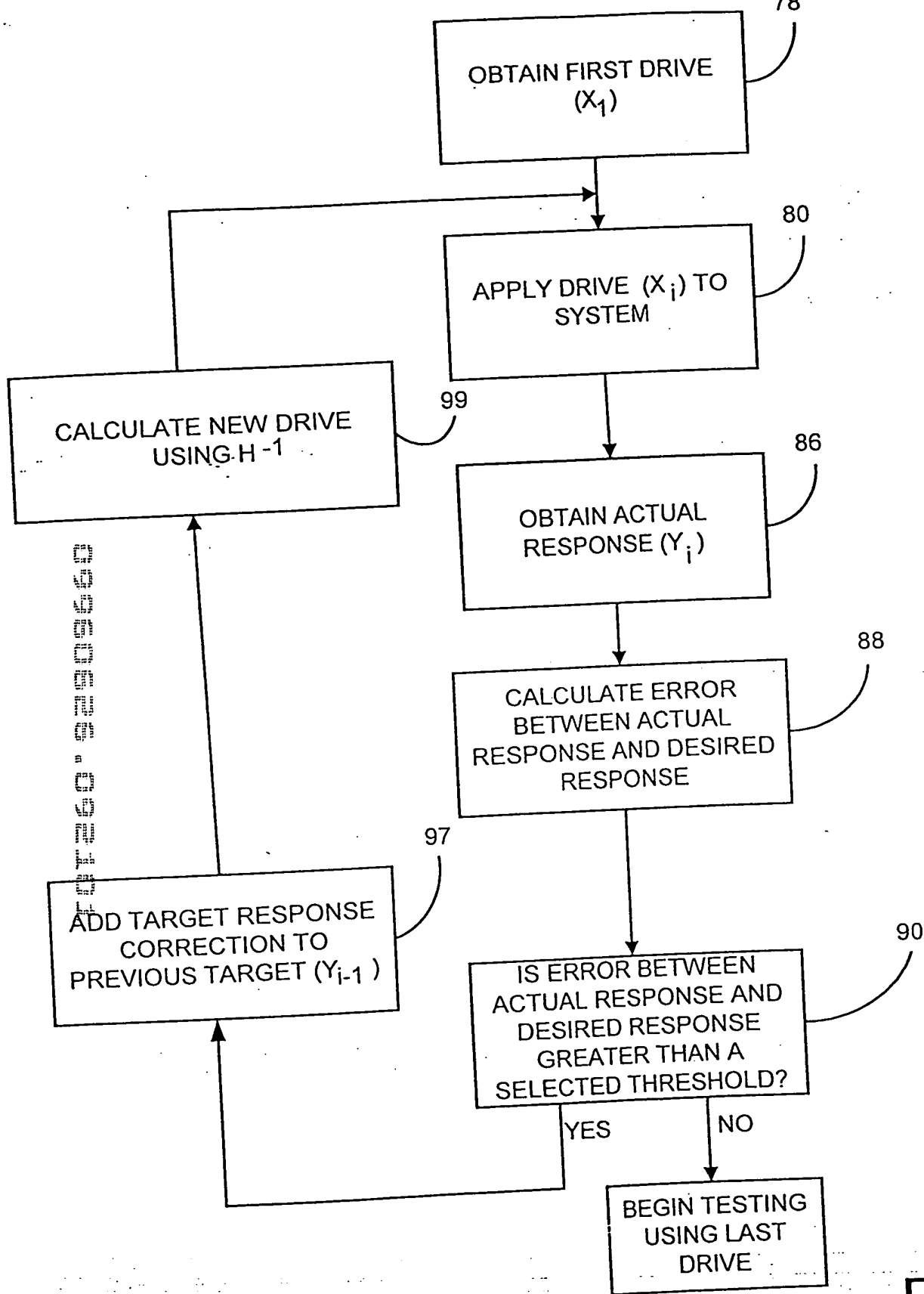


FIG. 3C

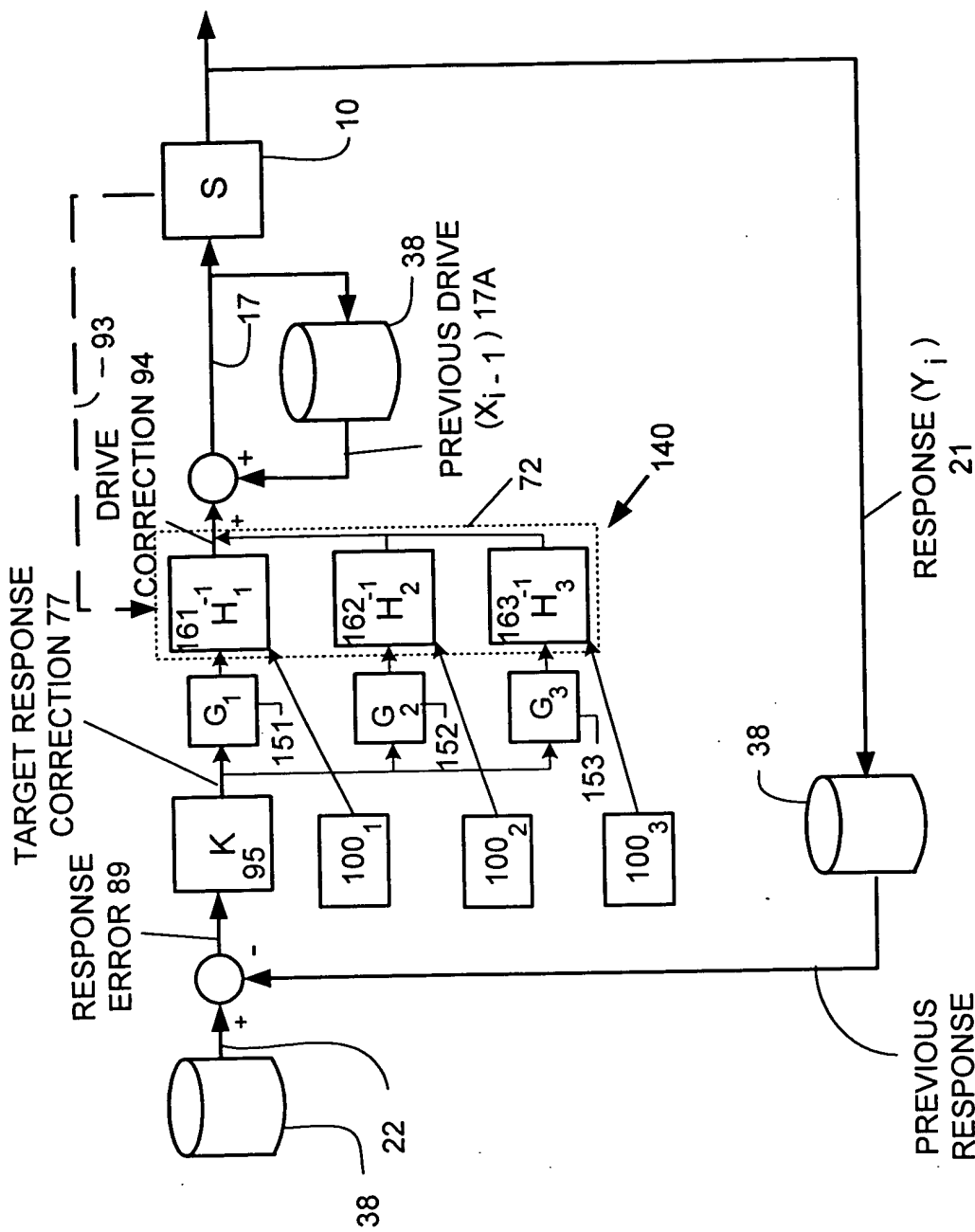


FIG. 4A

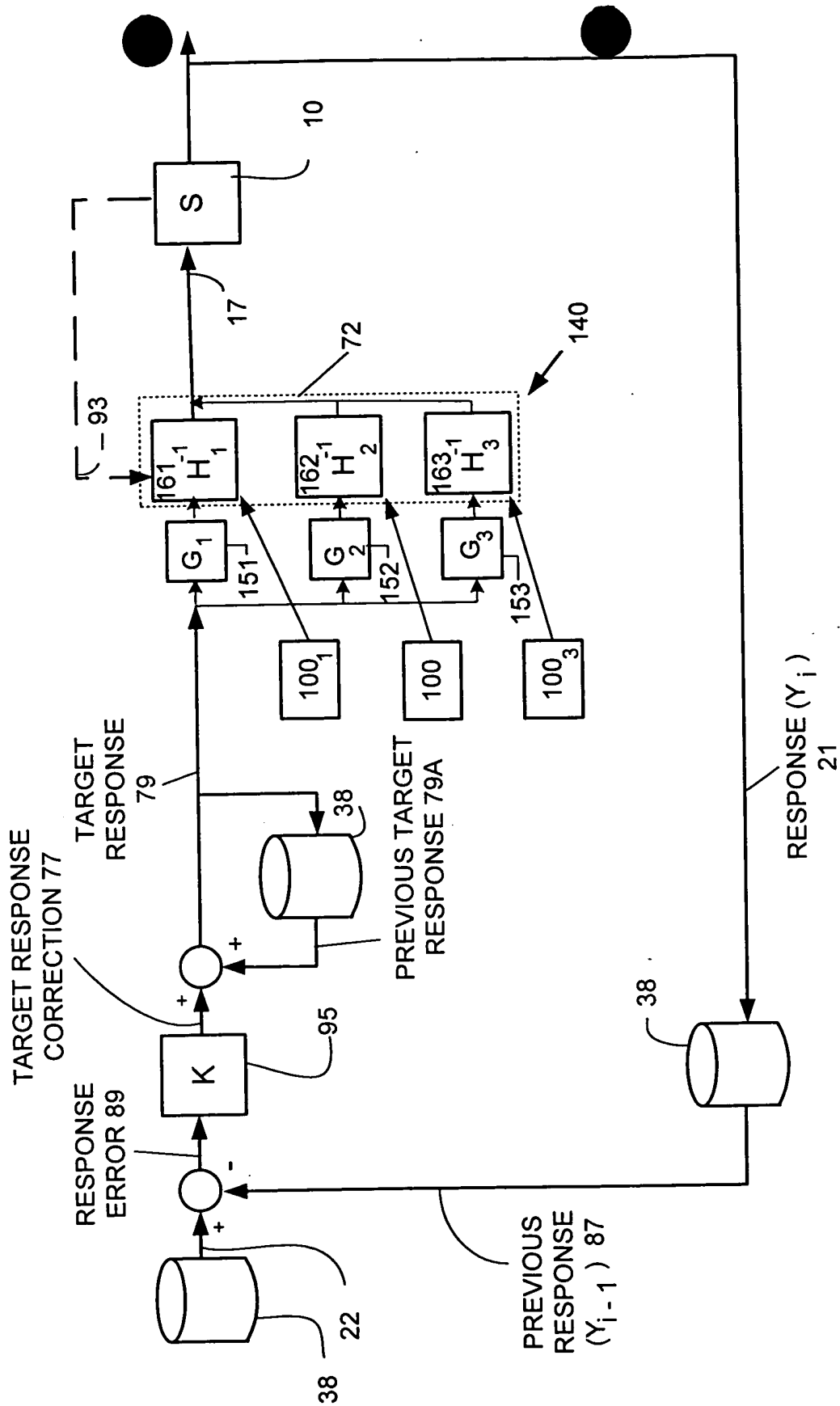


FIG. 4B

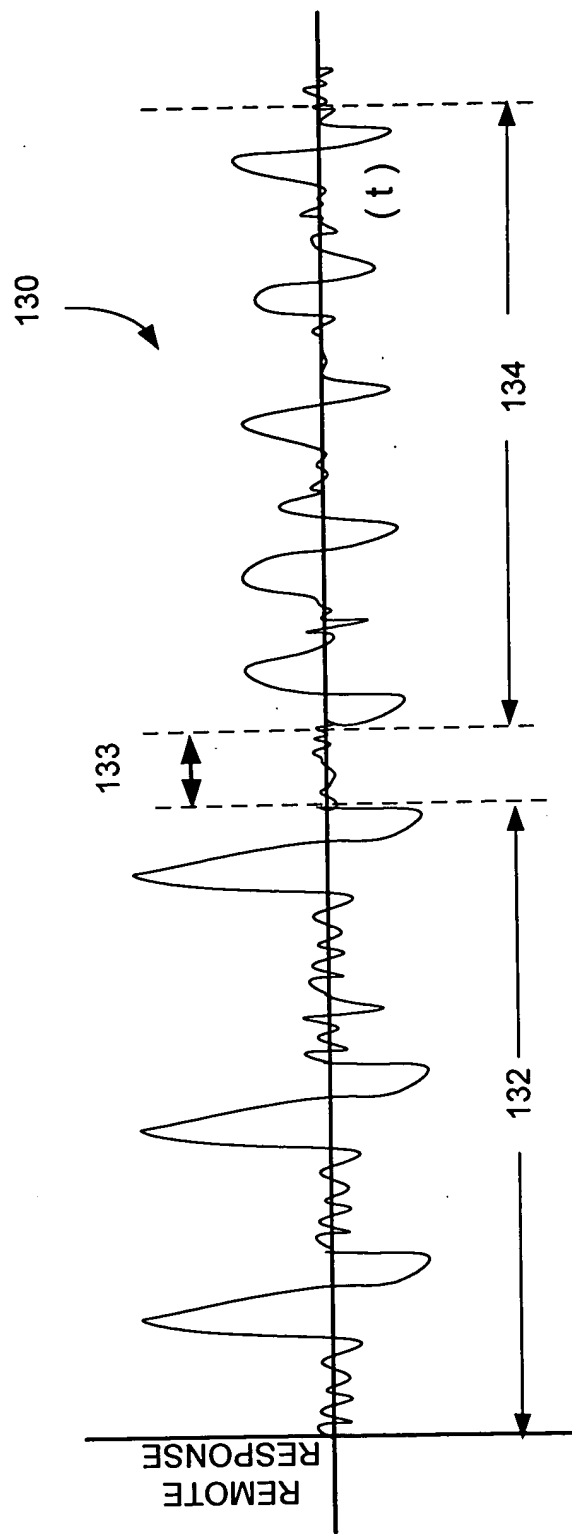


FIG. 5

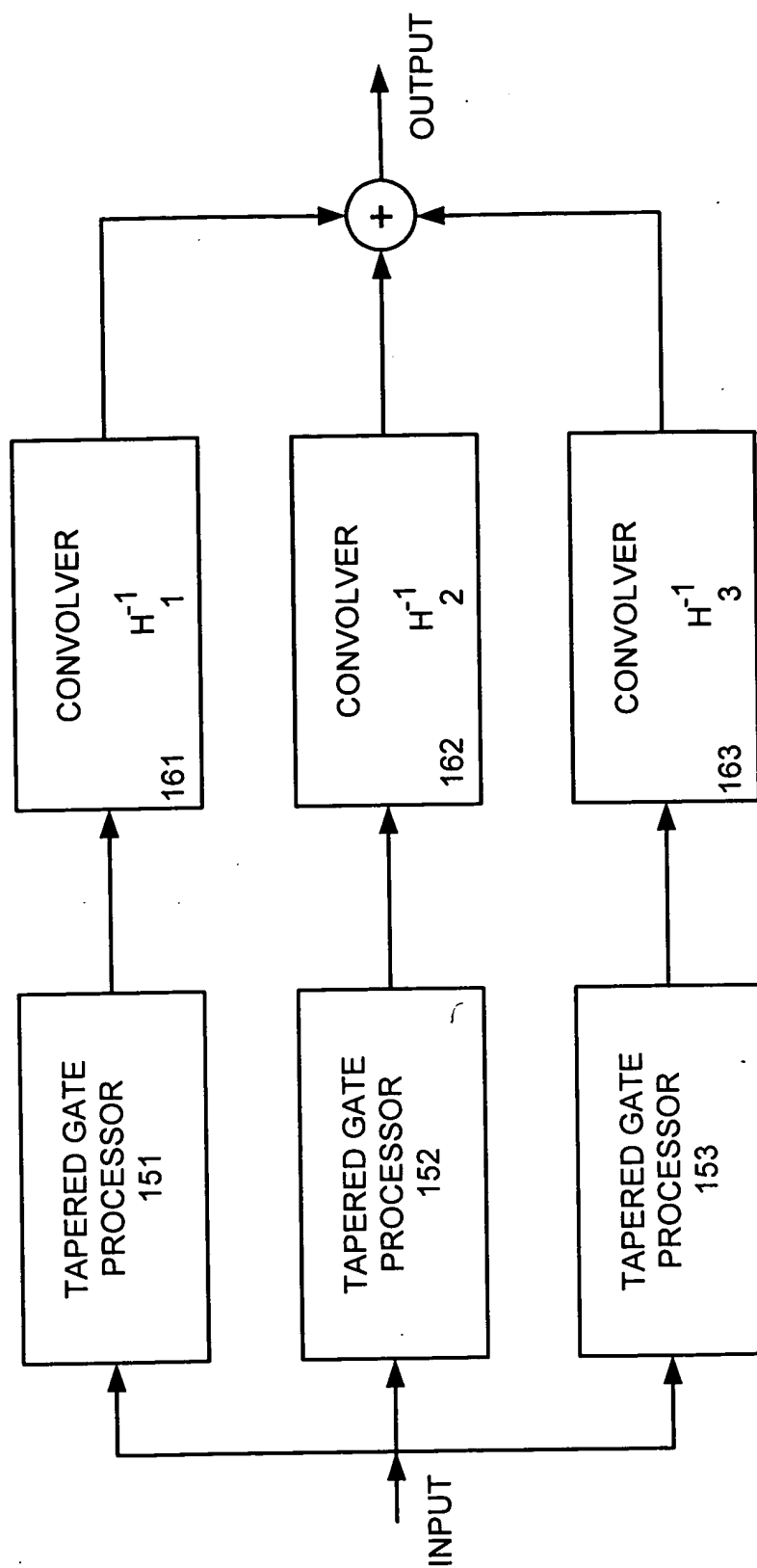


FIG. 6





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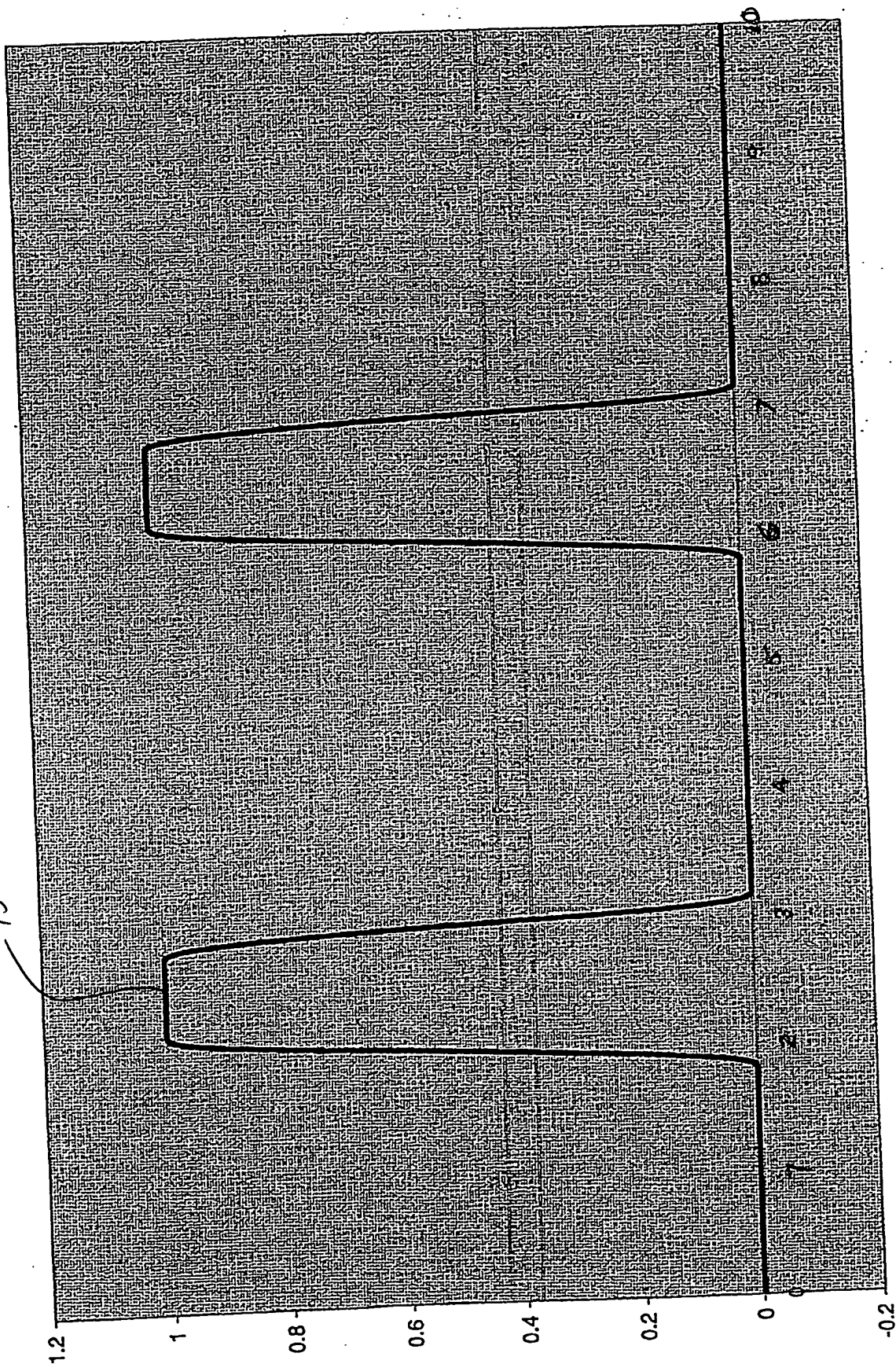
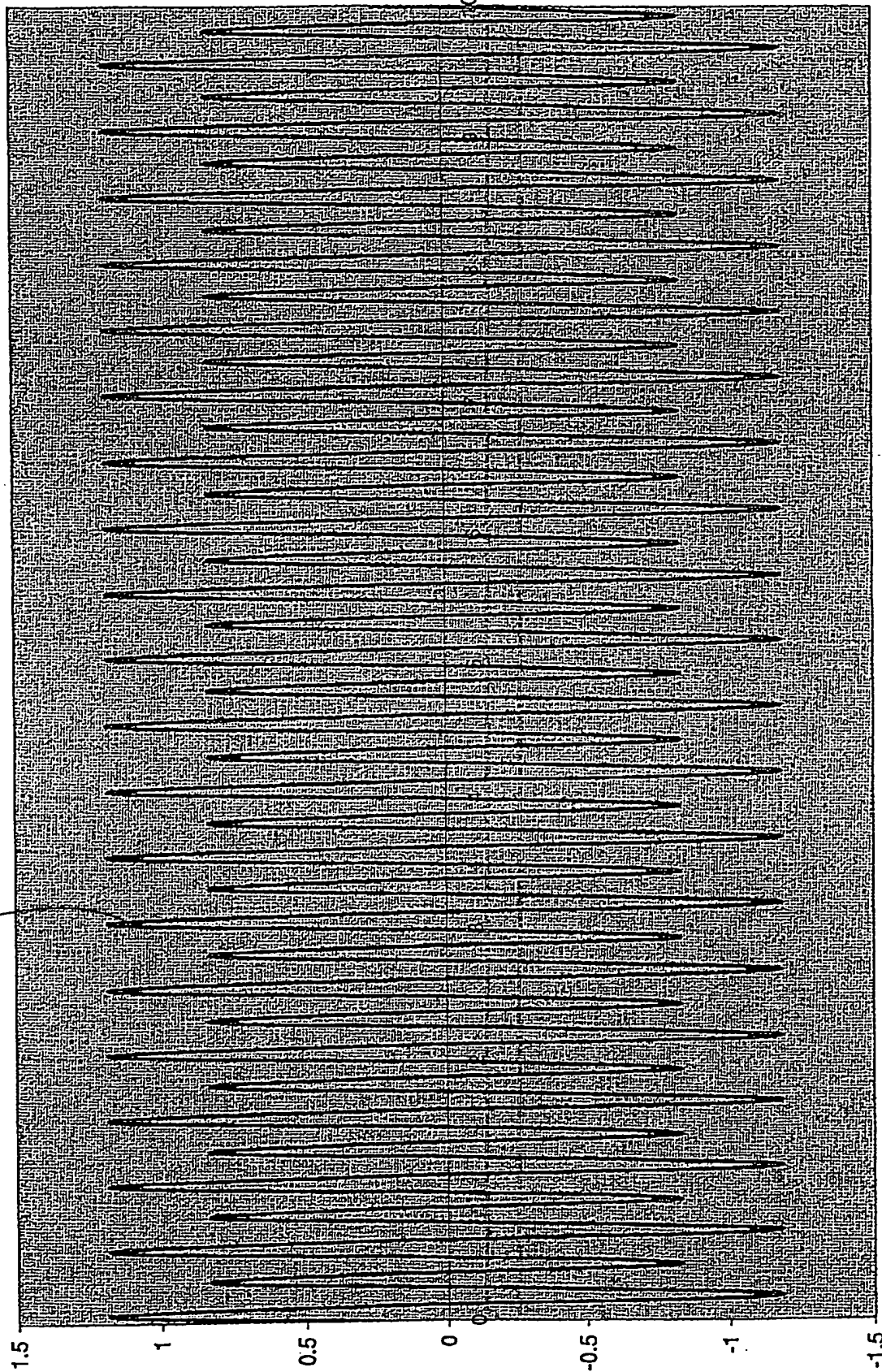


FIG. 8

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9 TB.



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- Gated Input Signal

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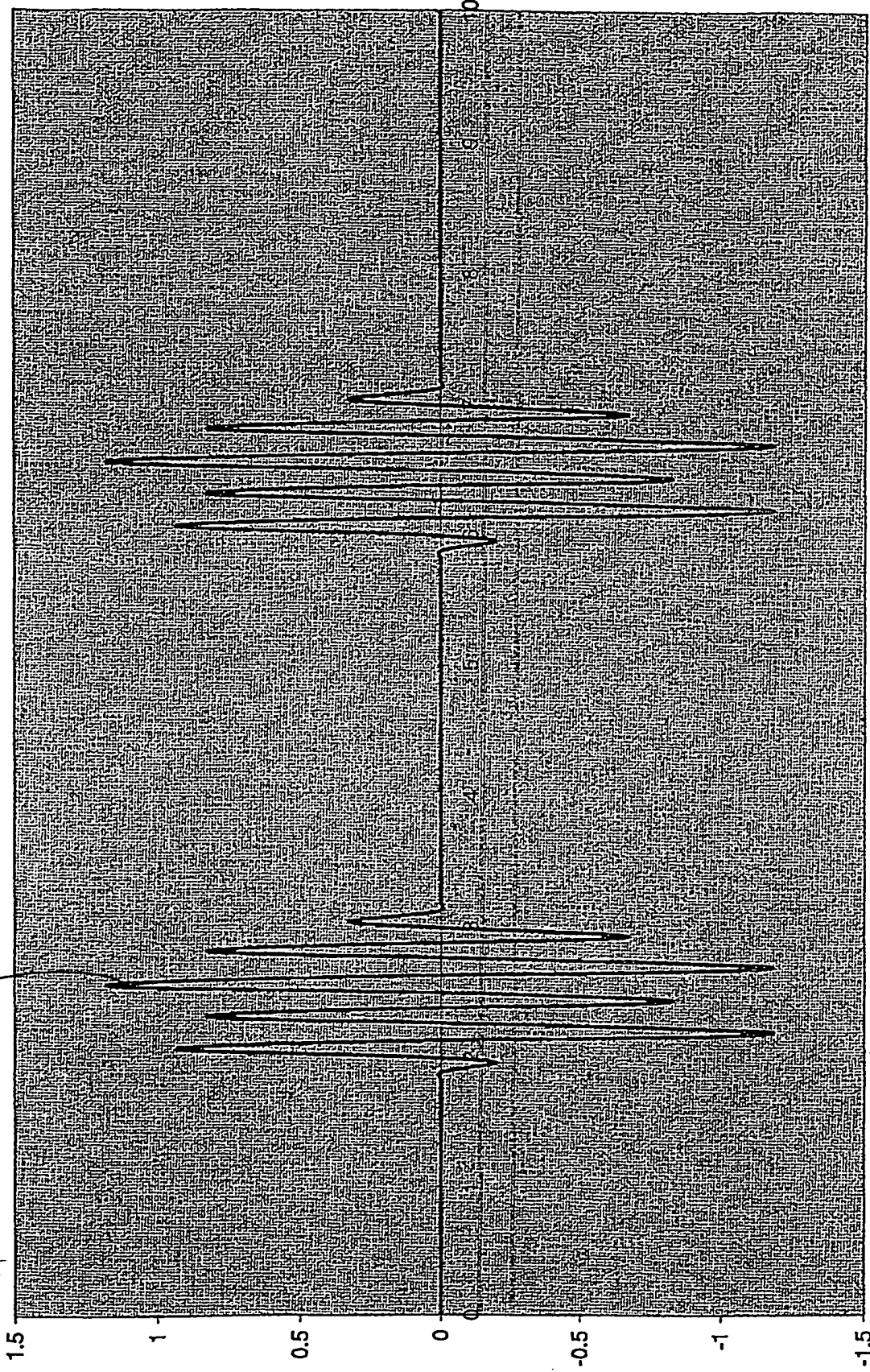
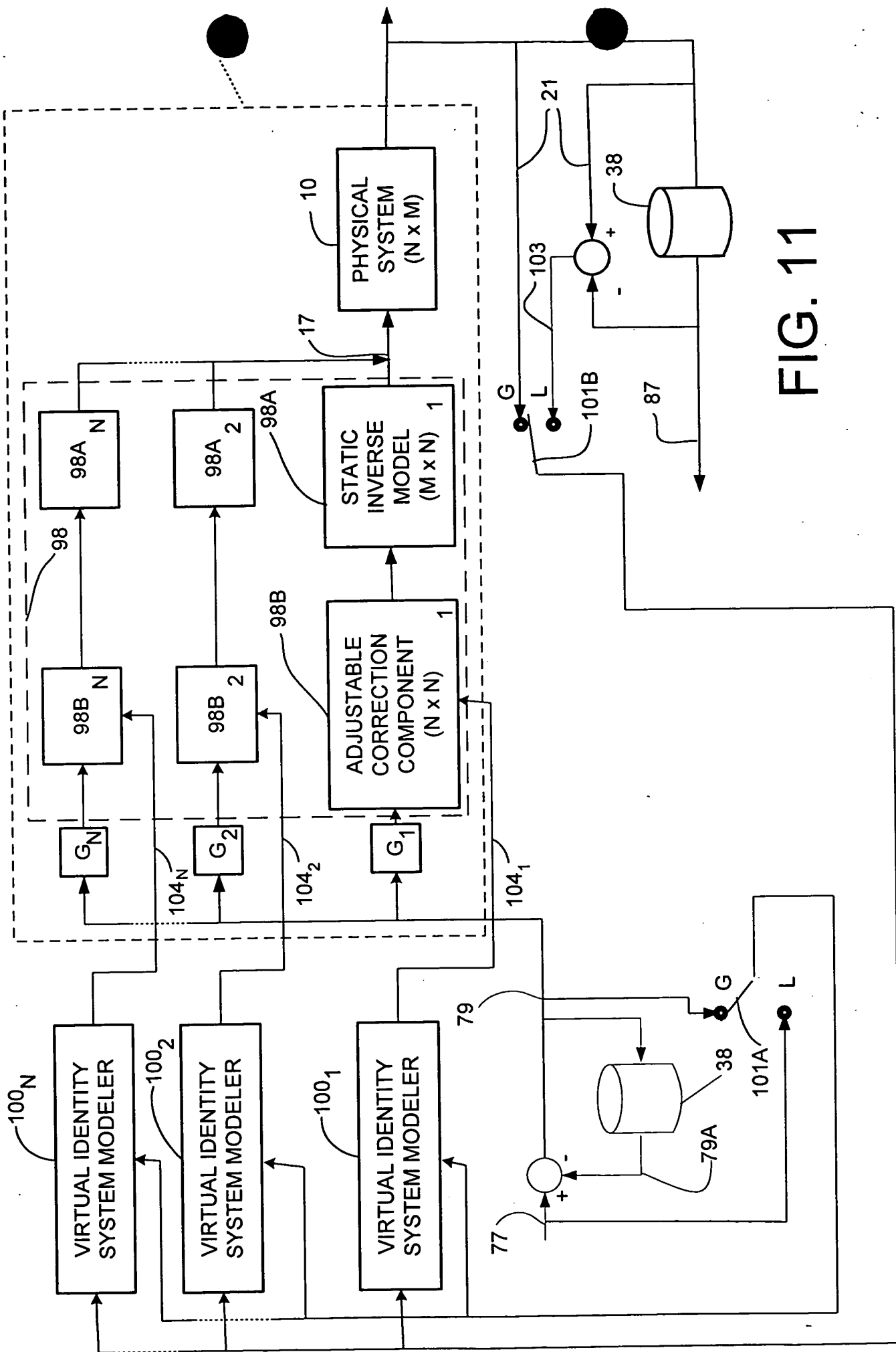


Fig. 10





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